IN THE SPECIFICATION:

Please rewrite the paragraph at page 6, line 26 to page 7, line 4 as follows:

The implementation of the functional replicator according to the invention, for which the various steps are shown by the flow chart in figure 2, begin with the definition and the selection of the functions (60) which are to be incorporated into the specific integrated circuit to be made. Selecting the non-programmable peripheral modules containing the desired functions makes this selection. The question remains, however, to know if these functions are all contained in the MP NP modules (62). If this is not the case, the missing functions must be added by programming one or more FPGA modules (64). This programming operation is generally made using a controller that programs the functions of the FPGA module when it is turned on owing to the code that was developed beforehand in a workstation.